

Amendments to the Specification:

Please replace ¶2 on page 4:

The present invention relates to clock signal buffering with increased ~~noise~~noise immunity in high speed digital systems. The following description is presented to enable one of ordinary skill in the art to make and use the invention and is provided in the context of a patent application and its requirements. Various modifications to the preferred embodiment and the generic principles and features described herein will be readily apparent to those skilled in the art. Thus, the present invention is not intended to be limited to the embodiment shown but is to be accorded the widest scope consistent with the principles and features described herein.